

KERALA AGRICULTURAL UNIVERSITY

B.Tech (Food . Engg) Degree Programme 2015 Admission

IInd Semester Final Examination- June – July 2016

Cat. No: Elen. 1201

Marks: 50.00

Title: Basic Electrical Engineering (2+1)

Time: 2 hours

I Answer all questions (10 x 1.0 = 10)

1. Collector base junction in PN junction is formed by _____
 2. Current is measured by the unit _____
 3. Cut in or threshold voltage for Silicon is: _____
 4. Power factor is defined as _____
 5. SCR is used for _____
- True or False**
6. Resistance of a conductor wire varies directly with its diameter .
 7. The property of a magnetic material by which it opposes the establishment of magnetic flux is called reluctance.
 8. Inductor is a device which stores electrical energy in electric field.
 9. Energy is measured by the instrument ammeter..
 10. Higher the SWG number, smaller is the diameter of wire.

II Answer any five questions only (5 x 2.0 =10)

1. Define the passive components of a circuit..
2. What is a PN junction diode and its practical usage
3. Explain the energy band diagram of materials.
4. Explain the mutual law of electro magnetic induction.
5. What is LED and where it is used.
6. Define DeMorgan s theorem
7. Write short notes on wiring plan.

III Answer any five questions only (5 x 4.0 =20)

1. Explain the input and output characteristics of NPN transistor
2. Explain about load estimation in a processing industry.
3. Explain the Network theorems.
4. Differentiate between Single phase and three phase AC circuits.
5. Explain about various types of Logic gates..
6. What is a Zener diode and explain it briefly.
7. Explain about the electrical tariff and safety..

IV Answer any one question only (1 x 10.0 =10)

1. a.Explain about the various special semi conductor devices..
b.Explain in detail about the star delta circuits.
2. a.Explain the power and energy measurement and load estimation in the processing industry.
b.Explain the working principle of SCR and its applications.